

**[PDF] The computer for the 21st century**

au.dk [PDF]

M Weiser - *Scientific American*, 1995 - [wiki.dream.au.dk](#)

... UBIQUITOUS COMPUTING begins to emerge in the form of live boards that **replace** chalkboards as well as in ... aug- ment main **memory** capacity: conserva- tive extrapolation of current technol- ogy suggests that **removable** hard disks (or nonvolatile **memory chips**) the size ...

Cited by 5307 - [Related articles](#) - [All 2 versions](#)**Adapter and a removable slide-in cartridge for an information storage system**

JC Dartien, MMA Praca, PJ Tomasi - US Patent 4,941,941, 1990 - Google Patents

... to be understood that the scope of this invention includes the application of a **removable cartridge** with ... the user to pull on the two hooks with his index fingers to remove and insert ... the housing 6 which houses the central processing unit, the disk drives, **RAM memory chips** and all ...

Cited by 145 - [Related articles](#) - [All 2 versions](#)**Removable mother/daughter peripheral card**

E Hagan, DC Guertman, RF Williams - US Patent App. 11/463,139, 2006 - Google Patents

... For example, they are intended to **replace** either of the existing fixed or **removable** floppy magnetic disk systems, or both. ... 11, 1991, **COMPUTER MEMORY CARD HAVING A LARGE NUMBER OF EEPROM INTEGRATED CIRCUIT CHIPS AND MEMORY SYS- TEMS** ...

Cited by 20 - [Related articles](#) - [All 2 versions](#)**Memory on the move**RM Sherwin - *IEEE Spectrum*, 2001 - [ieeeexplore.ieee.org](#)

... all the flash types are some vari- ety of electrically erasable programmable ROM (EEPROM) chip that maintains ... The **memory** cells are laid out in a grid of rows and columns, addressable by bit ... Data is writ- ten by using a process called tunneling to add or remove charge from the ...

Cited by 15 - [Related articles](#) - [All Cited](#) - [All 2 versions](#)**Flash memory goes mainstream**B Diptat, L Helbert - *IEEE spectrum*, 1993 - [ieeeexplore.ieee.org](#)

... Write operations to an EEPROM cell store or remove electrons from areas of the cell ... This added cell complexity, along with on-chip high-voltage generation and considerable peripheral logic ... density and in- creases cost per megabyte compared to other types of **memory storage**. ...

Cited by 16 - [Related articles](#) - [All Cited](#) - [All 2 versions](#)**Autonomous computer input device and marking instrument**

M Esperson - US Patent 5,247,137, 1993 - Google Patents

... and non-customized components which are commercially available from a number of different vendors of computer **chips**, printed circuit ... from the pen that the **memory storage** means is nearly filled to its capacity, the end-user could **replace** said **memory storage** module by ...

Cited by 10 - [Related articles](#)**User-removable central processing unit card for an electrical device**

S Lee, ... - US Patent 5,748,912, 1998 - Google Patents

... in time, the user can switch off power to the notebook PC, exchange user- removable CPU cards for example by **replacing** the x86 ... **removable** CPU card also has random-access-memory (in the form of cache **memory** SRAM chips and main **memory** DRAM chips) that can ...

Cited by 20 - [Related articles](#) - [All 2 versions](#)**Smart data storage device**

RM Eisele - US Patent 5,159,182, 1992 - Google Patents

... 1 includes **chip** 2(IC), eg. 45 head of the cassette-terminal, interface 6 is located ... such as verification of user authorization, en- changed it is no longer necessary to **replace** the card ... necessary to load the element's battery, and the battery provides the **device** with **memory** units with ...

Cited by 21 - [Related articles](#) - [All 2 versions](#)**SeeMote: In-situ visualization and logging device for wireless sensor networks**

psu.edu [PDF]

L Selavo, G Zilcu, JA Stankovic - 3rd International Conference ... 2006 - [ieeeexplore.ieee.org](#)

... two independent display windows and provides a list of logical graphical operations, such as **replace**, and, or ... up to 4GB **removable storage** for the MICAz based WSN nodes. ... The low level interface is very similar to the MICAz onboard flash **memory chip** interface, with which the ...

Cited by 13 - [Related articles](#) - [All 30 versions](#)**Secure and reliable bootstrap architecture**

psu.edu [PDF]

WA Arbough, DJ Farber, AD Kermeyer, JM ... - US Patent ... 2001 - Google Patents

... **STORAGE DRIVE 14** ... - **INTERFACE 20 REMOVABLE STORAGE UNIT 18 REMOVABLE STORAGE UNIT 22** ... Output System), any expansion 55 card ROMs, CMOS **memory** and NVRAM ... creates several configuration management problems, eg, a system upgrade requires the ...

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